

Electronic Acknowledgement Receipt

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Title of Invention:	Method of Manufacturing Multi-Layer Circuit Board
First Named Inventor/Applicant Name:	Toshiaki Takenaka
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	AttachA_IDS_P04030802.pdf	190311 236984c1b5d763d1cf51a607a63ed13e6ba11dd0	no	4

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